



MATLAB Embedded Coder for Intel Curie Platform

Nov 2016

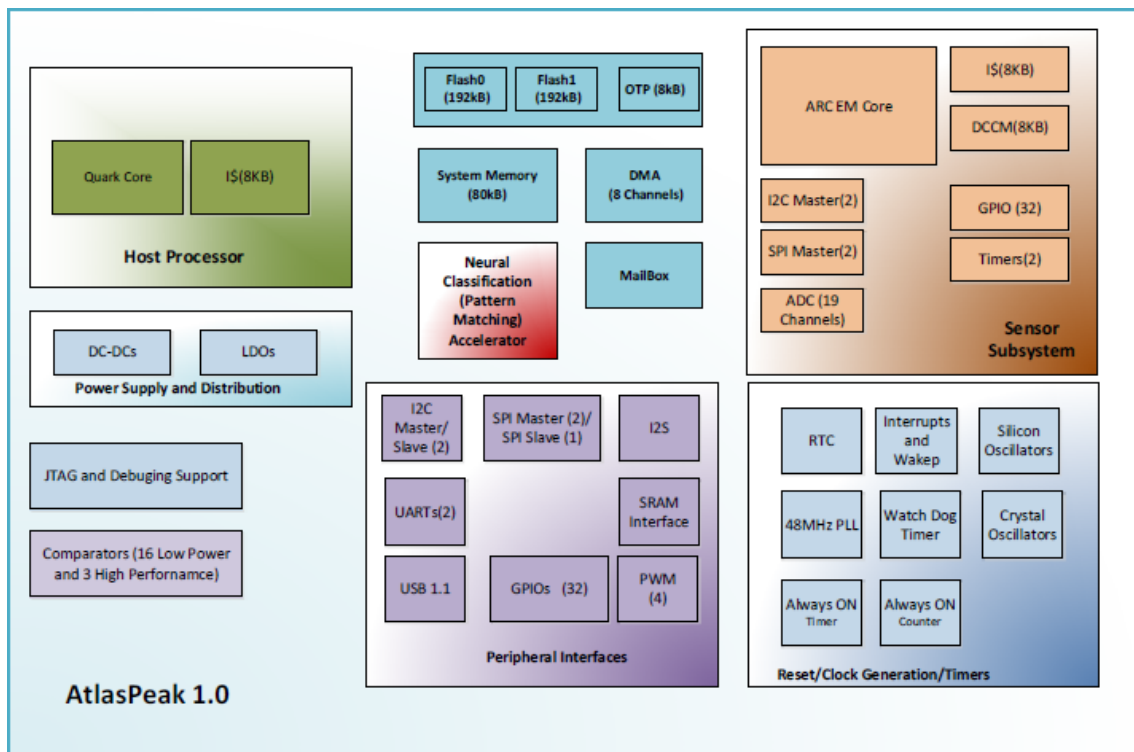
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Agenda

- Intel Atlas Peak Wearable SoC Platform Overview
- Curie Module and Arduino 101
- MATLAB Embedded Coder for Curie Platform
- Case Study – Biking Trick Classifier
- Summary

Atlas Peak SoC Platform Overview



Quark Core (LMT)

- 32 MHz Clock Frequency
- 32-bit Address Bus
- 8 kB L1 Instruction Cache

ARC EM Core - Sensor Subsystem

- 32 MHz Clock Frequency
- I2C Master, SPI Master, ADC, GPIO, timers
- 8 kB L1 Instruction Cache

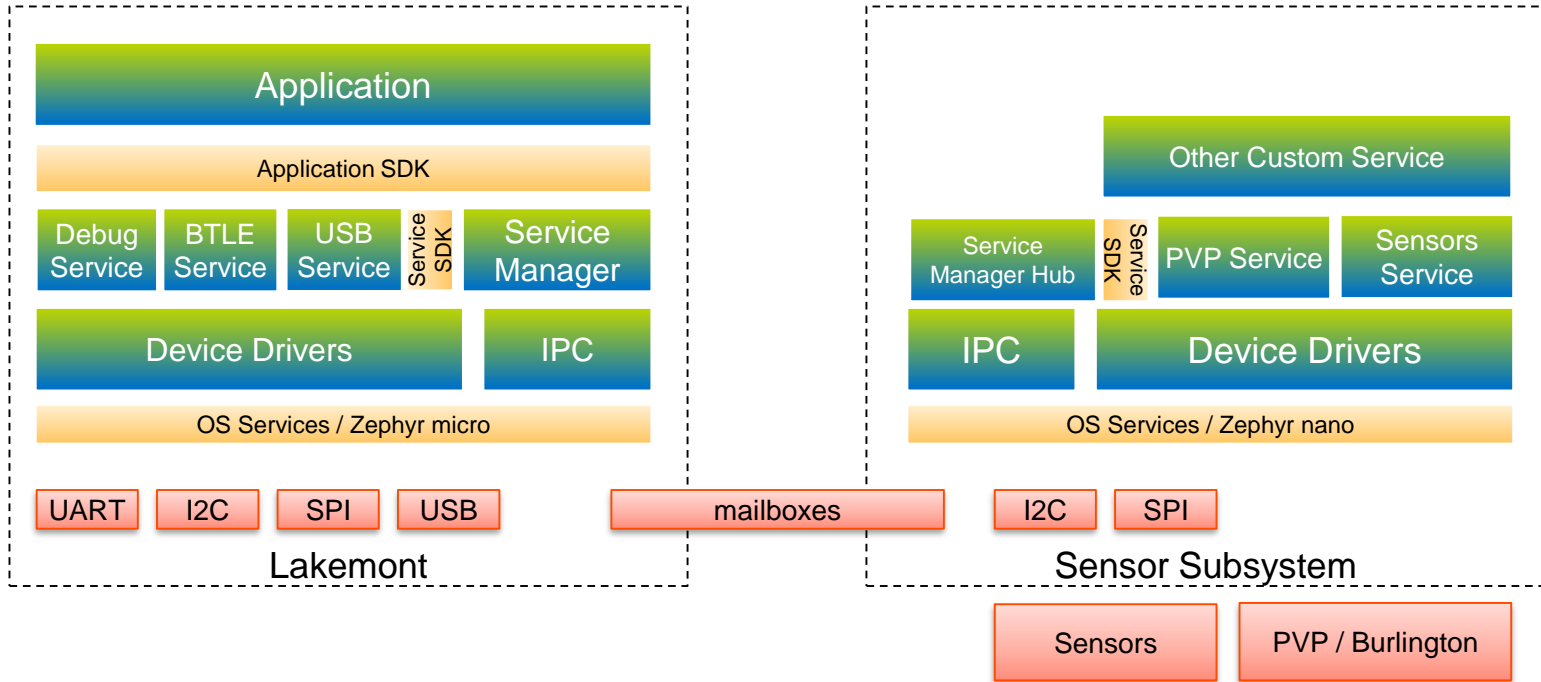
PVP – Pattern Matching Accelerator

- Parallel data recognition engine
- 128 Neurons with 128 features per Neuron

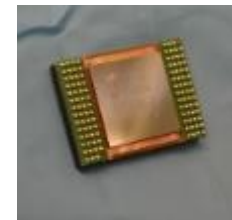
On-Die Memory

- 384 kB of on-die Flash
- 80 kB of on-die SRAM

Atlas Peak SW Architecture



Curie Module and Arduino 101

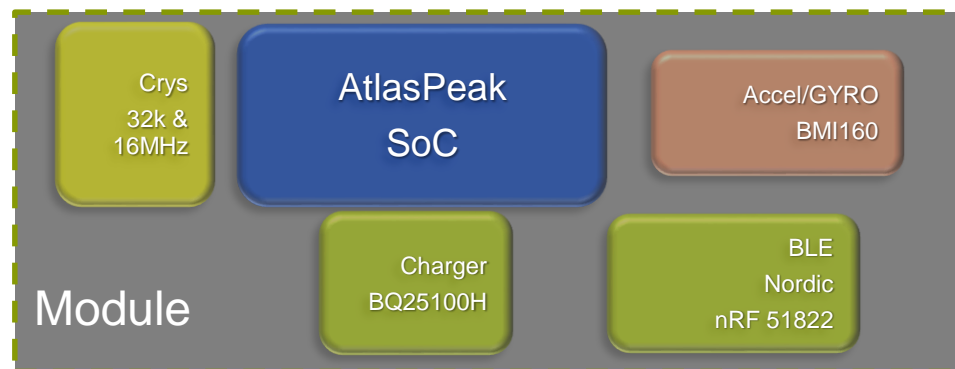


Physical

Form Factor	BGA module
Dimensions	8x10x2mm (TBD)
SoC	AtlasPeak 1.0, 32MHz
Storage	NA
Memory	within SoC, 80KB RAM
Sensors	Accel/Gyro,

Key EE components

SoC	AtlasPeak1.0
Sensor	6 Axis BMI160
Charging	BQ25101
BLE	Nordic nRF51822



Arduino 101

<https://www.arduino.cc/en/Main/ArduinoBoard101>

Open Source FW Code:

<https://github.com/CurieBSP>



PVP Pattern Matching Acceleration

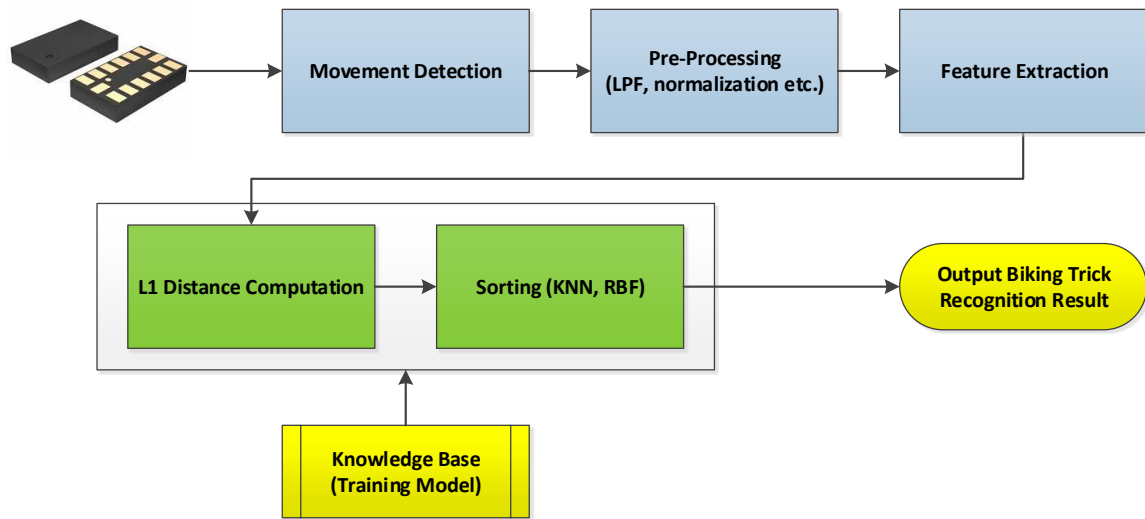
- Highly parallel 128-vector distance computation and sorting
- 16KB of closely coupled memory attached to the neurons for high throughput
- Input – vector to be recognized
- Output – fired neurons or the nearest neighbors
- Computationally intensive modules
 - Distance computation between the input vector and reference vector
 - Sorting to find the nearest neighbor, especially for kNN & RBF

MATLAB Embedded Coder for Curie Platform

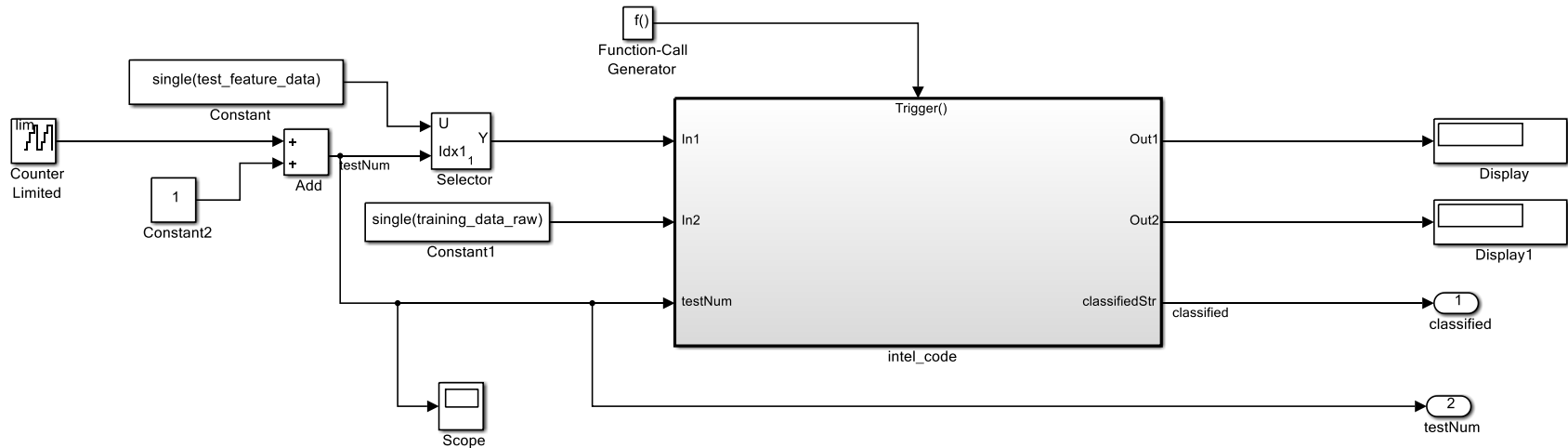
- Optimization and code configuration options from Mathworks
- Multi-task and multicore code execution
- Best utilize features on Curie platform for quick prototyping
 - x86 Quark CPU
 - ARC DSP with FPU and DSP extension
 - PVP pattern matching HW accelerator
 - Optimized math library for ARC

Case Study – Biking Trick Classifier

- Biking trick: type = {'UnKnown'; 'Big Air'; 'Tobogan'; 'BackFlip'; 'XUp'; 'TailWhip'};



Biking Trick Classifier – Simulink



**Generate Code Using
Simulink Coder
(double-click)**

**Generate Code Using
Embedded Coder
(double-click)**

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Biking Trick Classifier – Quark/ARC

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Generated Code

[-] Main file

[ert_main.c](#)

[-] Model files

[demo_model.c](#)

[demo_model.h](#)

[demo_model_private.h](#)

[demo_model_types.h](#)

[-] Data files

[demo_model_data.c](#)

[+] Utility files (1)

```
11  * Embedded hardware selection: Intel->x86-32 (Windows32)
12  * Code generation objectives: Unspecified
13  * Validation result: Not run
14  */
15
16  #include "demo_model.h"
17
18  static boolean T OverrunFlag = 0;
19
20  /* Base rate step function */
21  void rt_OneStep()
22  {
23      /* Check for overrun */
24      if (OverrunFlag++) {
25          rtmSetErrorStatus(demo_model M, "Overrun");
26          return;
27      }
28
29      /* Step the model for base rate */
30      demo_model step();
31
32      /* Get model outputs here */
33      OverrunFlag--;
34
35      /* Disable interrupts here */
36      /* Restore FPU context here (if necessary) */
37      /* Enable interrupts here */
38  }
39
40  /* Genesis target Main function */
41  void mw_arc_initialize(void)
42  {
43      /* Initialize model */
44      demo_model initialize();
45  }
46
47  /* Genesis target Main function */
48  void mw_arc_terminate(void)
49  {
50      /* Terminate model */
51      demo_model terminate();
52  }
53
```

Biking Trick Classifier - PVP

DRIVER_API_RC qrk_cxxxx_pvp_recognize_vector(...)

DRIVER_API_RC qrk_cxxxx_pvp_save_knowledge(...)

DRIVER_API_RC qrk_cxxxx_pvp_load_knowledge(...)

DRIVER_API_RC qrk_cxxxx_pvp_set_config(...)

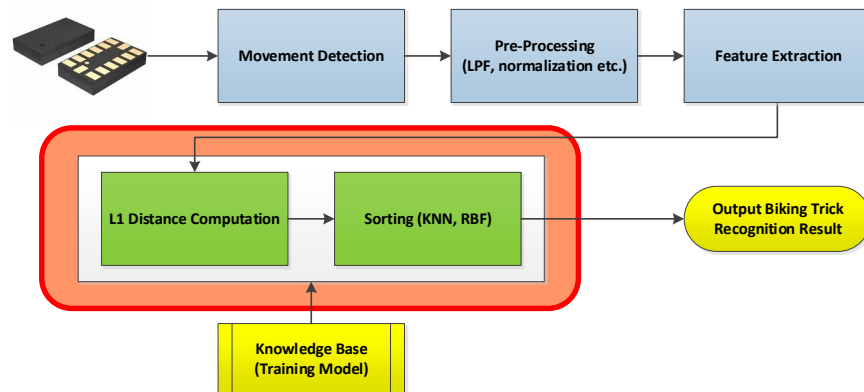
DRIVER_API_RC qrk_cxxxx_pvp_get_config(...)

DRIVER_API_RC qrk_cxxxx_pvp_reset_database(...)

DRIVER_API_RC qrk_cxxxx_pvp_read_neuron(...)

DRIVER_API_RC qrk_cxxxx_pvp_clock_enable(...)

DRIVER_API_RC qrk_cxxxx_pvp_clock_disable(...)



Demo Session

- Run Biking Trick Classifier purely in MATLAB Simulink.
- Embedded Coder generates the ARC optimized C code, and then uses Curie platform toolchain to compile and generate the library.
- Library is further built with CurieBSP firmware codebase and becomes part of firmware image.
- Flash the newly generated image to Arduino 101, and the same result is shown.

Summary

- Intel Atlas Peak SoC brings extremely low power and rich feature set for wearable and IOT segments.
- MATLAB Embedded Coder supporting for Atlas Peak platform enables broader eco-system adoption for quick prototyping, code and algorithm optimization.